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FACSIMILE TRANSMITTAL SHEET

In re application of: Shi-Chi Lin

Examiner: José R. Diaz

Serial No.: 10/650,601

Group Art Unit: 2815

Filed: 08/28/2003

Confirmation No.: 8321

For: METHOD OF MANUFACTURING DIELECTRIC ISOLATED SILICON
STRUCTURE

To: Examiner José R. Diaz

FIRM/COMPANY: Commissioner for Patents
Mail Stop RCE
P.O. Box 1450
Alexandria, VA 22313-1450

FACSIMILE NUMBER: 571-273-8300

FROM: Mark J. Marcelli

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DATE: January 16, 2006

TOTAL # OF PAGES:
(INCLUDING COVERSHEET)MESSAGE: Attached is a 1) Request for Examination Under 37 CFR
§1.114; 2) Petition for Extension of Time (One Month) and
3) Copy of previously filed Amendment filed on December 2,
2005.

NOTE: Original will not follow

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